

IN THE CLAIMS

Please find below a listing of all of the pending claims. The status of each claim is set forth in parentheses. This listing will replace all prior versions, and listings, of claims in the present application.

1. (Currently amended) An apparatus for communicating graphics ~~between at least two remotely located computers~~ across a computer network from a source computer, said apparatus comprising:
- a connector for being plugged directly into a slot of the source computer;
 - an input for receiving a video signal output from a graphics card of [[a]]the source computer through the connector;
 - a memory for storing discrete units of the video signal;
 - a compression circuit for compressing a plurality of the discrete units into a compressed video signal; and
 - a network interface circuit coupled to [[both]] the compression circuit and the computer network, the network interface circuit configured to format the compressed video signal into a plurality of Internet Protocol (IP) packets and to communicate the IP packets compressed video signal over the computer network, wherein the input, memory, compression circuit, and the network interface circuit are contained on a single card to a remote computer; and
 - an output coupled to the computer network.

2. (Currently amended) An apparatus for communicating graphics across a computer network ~~from a source computer, said apparatus~~ comprising:

a connector for being plugged directly into a slot of the source computer;

an input for receiving a video signal from the source computer through the connector;

a memory for storing discrete units of the video signal;

a compression circuit for compressing a plurality of the discrete units into a compressed video signal; and

a network interface circuit coupled to ~~[[both]]~~ the compression circuit ~~and the computer network~~, the network interface circuit ~~configured~~ to format the compressed video signal into a plurality of Internet Protocol (IP) packets and to communicate the IP packets ~~compressed video signal over the computer network to a remote computer.~~

3. (Original) The apparatus of claim 2, wherein the video signal is in compliance with a Digital Visual Interface (DVI) standard.

4. (Original) The apparatus of claim 2, wherein the video signal is an analog video signal.

5. (Original) The apparatus of claim 2, further comprising a circuit for converting an analog video signal into a digital video signal.

Claims 6-8. (Canceled).

9. (Original) The apparatus of claim 2, further comprising a second input for receiving a second video signal.

10. (Original) The apparatus of claim 9, wherein the compression circuit is further configured to separately compress a plurality of discrete units for each of the video signals.

11. (Currently amended) The apparatus of claim 2, wherein the network interface circuit is configured to format and communicate IP packets of separately compressed video signals to different remote computers, such that a first remote computer receives a first set of IP packets ~~compressed video signal~~ and a second remote computer receives a second set of IP packets ~~compressed video signal~~.

12. (Currently amended) The apparatus of claim 2, further comprising a plurality of network interface circuits, each network interface circuit being coupled to ~~[[both]]~~ a compression circuit ~~and the computer network~~, each network interface circuit being configured to format and communicate the IP packets ~~compressed video signal~~ over the computer network ~~to a remote computer~~.

13. (Currently amended) The apparatus of claim 2, ~~wherein the apparatus comprises a connector for direct connection to a source computer that supplies the video signal~~, wherein the connector ~~comprises signals carrying~~ is to carry power signals for powering the apparatus from the source computer.

14. (Currently amended) The apparatus of claim [[13]]2, ~~wherein the slot comprises a card slot and~~ wherein the connector is an edge connector ~~configured~~ to directly plug into [[a]]the card slot of [[a]]the motherboard ~~of the source computer.~~

Claims 15-18. (Canceled).

19. (Currently amended) A method for communicating graphics across a computer network from a source computer having a slot, said method comprising:

implementing an apparatus directly connected to the slot of the source computer by a connector, wherein the apparatus comprises a memory, a compression circuit, and a network interface circuit contained on a single card by:

receiving a video signal from a graphics card of [[a]] source computer through the connector;

converting the video signal into a plurality of Internet Protocol (IP) packets [[format]] suitable for communication over [[a]]the computer network using the network interface circuit; and

communicating the converted video signal as the plurality of IP packets across the computer network through a network connection of the source computer to a remote computer.

20. (Currently amended) The method of claim 19, ~~wherein the step of converting comprises forming a plurality of Internet Protocol (IP) packets collectively embodying the video signal~~ further comprising:

storing the received video signal in the memory prior to converting the video signal.

21. (New) The method of claim 19, wherein the video signal comprises a plurality of discrete units, said method further comprising:

compressing, using the compression circuit, the plurality of discrete units into a compressed video signal and wherein converting the video signal further comprises converting the compressed video signal.

22. (New) The apparatus of claim 1, wherein the apparatus comprises a peripheral component interface-based board, wherein the slot of the source computer comprises a peripheral component interface expansion slot, and wherein the connector of the apparatus is to connect directly into the expansion slot.

23. (New) The apparatus of claim 1, wherein the compression circuit and the network interface circuit comprises a devices selected from the consisting of a digital signal processor, a floating-point gate array, and an application-specific integrated circuit.

24. (New) The apparatus of claim 1, wherein the slot comprises a card slot and wherein the connector is an edge connector to directly plug into the card slot of the motherboard.

25. (New) The apparatus of claim 2, wherein the apparatus comprises a peripheral component interface-based board, wherein the slot of the source computer comprises a peripheral

component interface expansion slot, and wherein the connector of the apparatus is to connect directly into the expansion slot.

26. (New) The apparatus of claim 2, wherein the compression circuit and the network interface circuit comprises a devices selected from the consisting of a digital signal processor, a floating-point gate array, and an application-specific integrated circuit.

27. (New) The apparatus of claim 2, wherein the network interface circuit is to communicate the IP packets over the computer network by communicating the IP packets through a network connection of the source computer.